Overview and Activities

Thomas Steinke (vice president)

IXPUG BoF at SC21

www.ixpug.org

@IXPUG1

Fosters technical collaboration

Independent users group

Free exchange of information

System architecture

Software tools
Programming models

New workloads

IXPUG
INTEL EXTREME PERFORMANCE USERS GROUP

Enhancing HPC & AI/DL workloads

Freely exchanging best practices publicly

Strong technical support from Intel experts

Computing systems using Intel® architecture & technologies

IXPUG LEADERSHIP BOARD

President: R. Glenn Brook

Cornelis Networks

Vice President: Thomas Steinke

Zuse Institute Berlin

Secretary: Amit Ruhela

Texas Advanced Computing Center

IXPUG STEERING COMMITTEE

Aksel Alpay
Heidelberg University

Clayton Hughes
Sandia National Laboratories

David Martin
Argonne National Laboratory

Hatem Ltaief
KAUST

James Lin
Shanghai Jiao Tong University

Joe Zerr
Los Alamos National Laboratory

Maria Girone
CERN openlab

Melyssa Fratkin
TACC

Nalini Kumar
Intel Corporation

Toshihiro Hanawa
The University of Tokyo

REGULAR IXPUG ACTIVITIES

- Annual Meeting
- BOF / Panels / Workshops
  - SC
  - ISC
  - HPC Asia
- Software-Defined Visualization Workshop
- Webinars

IXPUG Workshop @ HPC Asia 2022

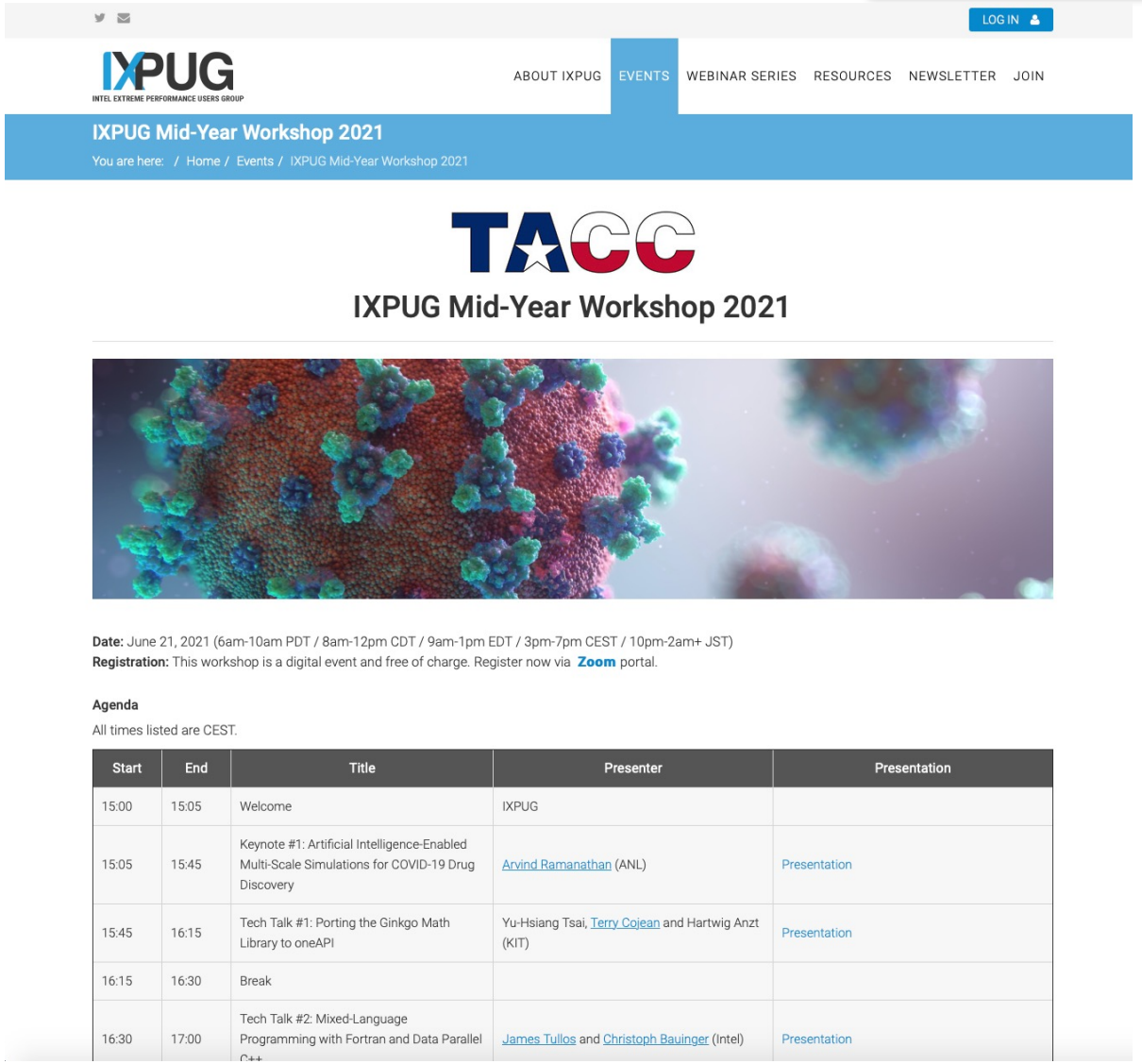
January 14th 2022, DIGITAL

https://www.cspp.cc.u-tokyo.ac.jp/hanawa/pukiwiki/index.php?IXPUG-HPCAsia2022

RESOURCES

Workshop slides/recordings and Webinars →

https://www.ixpug.org/resources



HOW TO GET INVOLVED

- Join IXPUG (www.ixpug.org)
- Participate in an IXPUG Webinar
- Offer to give a presentation!
- Attend an IXPUG Meeting
- Host an IXPUG Meeting
- Volunteer for the IXPUG Steering Committee

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