



The **I**ntel **eX**treme **P**erformance **U**sers **G**roup (IXPUG) is an independent users group whose mission is to provide a forum for the free exchange of information that enhances the usability and efficiency of scientific and technical applications running on large High Performance Computing (HPC) systems using the Intel® architecture. IXPUG is administered by representatives of member sites that operate large HPC systems.



What is IXPUG?

- Now Intel eXtreme Performance Users Group
- Global community-driven organization (independently run)
- Fosters technical collaboration tuning for Intel® architecture, using:
 - **System hardware beyond the processor** (memory, interconnect)
 - **Software tools and programming models**
 - **New workloads** (HPC, data analytics, AI, visualization, etc.)
- Freely exchanging best practices, experiences worldwide (open to the public)
- Strong technical support from industry and Intel experts

17 major events in ~2 years!



IXPUG
INTEL EXTREME PERFORMANCE USERS GROUP



IXPUG Annual Conference 2019: Call for Abstracts & Registration are open!



Join Intel and other industry experts at 4-day,
interactive, open forum

Event Description: This IXPUG conference is focused on all aspects of adopting and employing state-of-the-art data-processing technologies and techniques for optimal application execution. This includes accelerators (e.g. FPGAs, GPUs), as well as topics related to system hardware beyond the processor (memory, interconnect, etc.), software tools, programming models, new workloads, and more — all with a focus on Intel platforms.

Call for Abstracts deadline: July 8, 2019

Conference dates: September 24–27, 2019

Venue and location: Globe of Science and Innovation at CERN (Geneva, Switzerland)

Registration: Seating is limited. Advance registration is required. Visit www.ixpug.org/events for info.



Photo courtesy of CERN