

oneAPI for IXPUG

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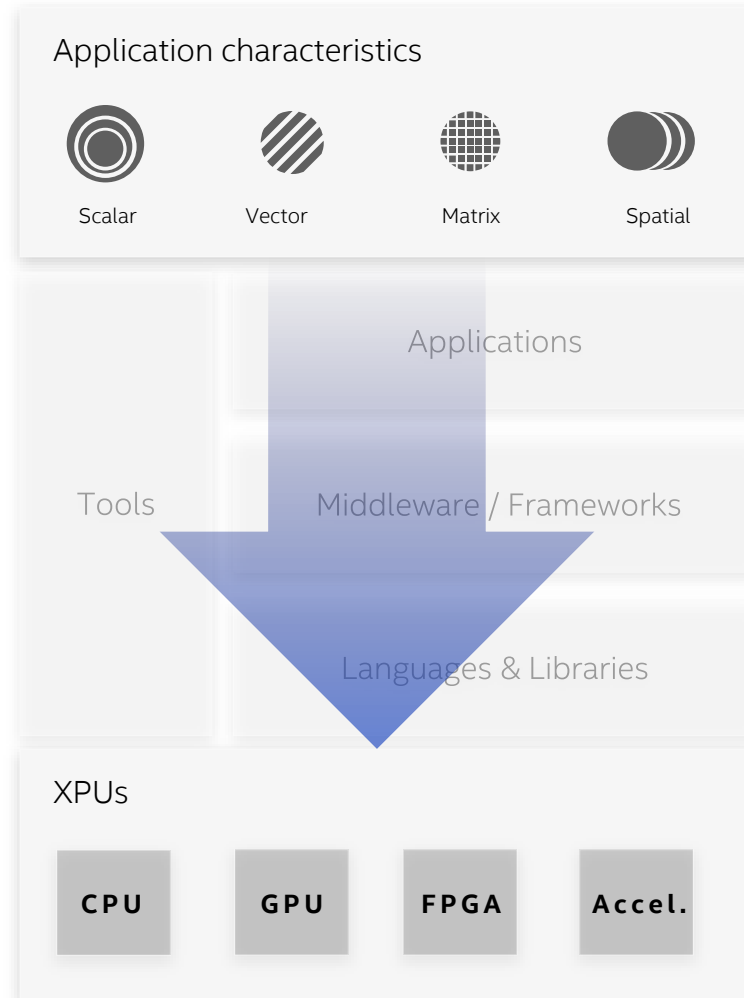
We need your feedback

- A unique opportunity to steer the parallel programming ecosystem
- A problem worth solving
 - Multi-architecture, avoiding lock-in to 1 specific hardware architecture
 - Direct and library-based programming
 - Extending existing models
 - Performant
- Your input is important

PROGRAMMING CHALLENGES FOR MULTIPLE ARCHITECTURES

Variety of compute patterns

Diverse set of data-centric hardware required



No common programming language or APIs

Inconsistent tool support across platforms

Each platform requires unique software investment

What if

- For application developers
 - A single highly productive **language** and a rich set of **libraries** was available **everywhere**
- For HW vendors
 - Tap into **existing SW ecosystem**
 - Define their own “to the metal” language
- For tool developers
 - Leverage existing **compiler infrastructure** and tooling interfaces

oneAPI

A unified programming model
to simplify development
across diverse architectures

Common developer experience across architectures

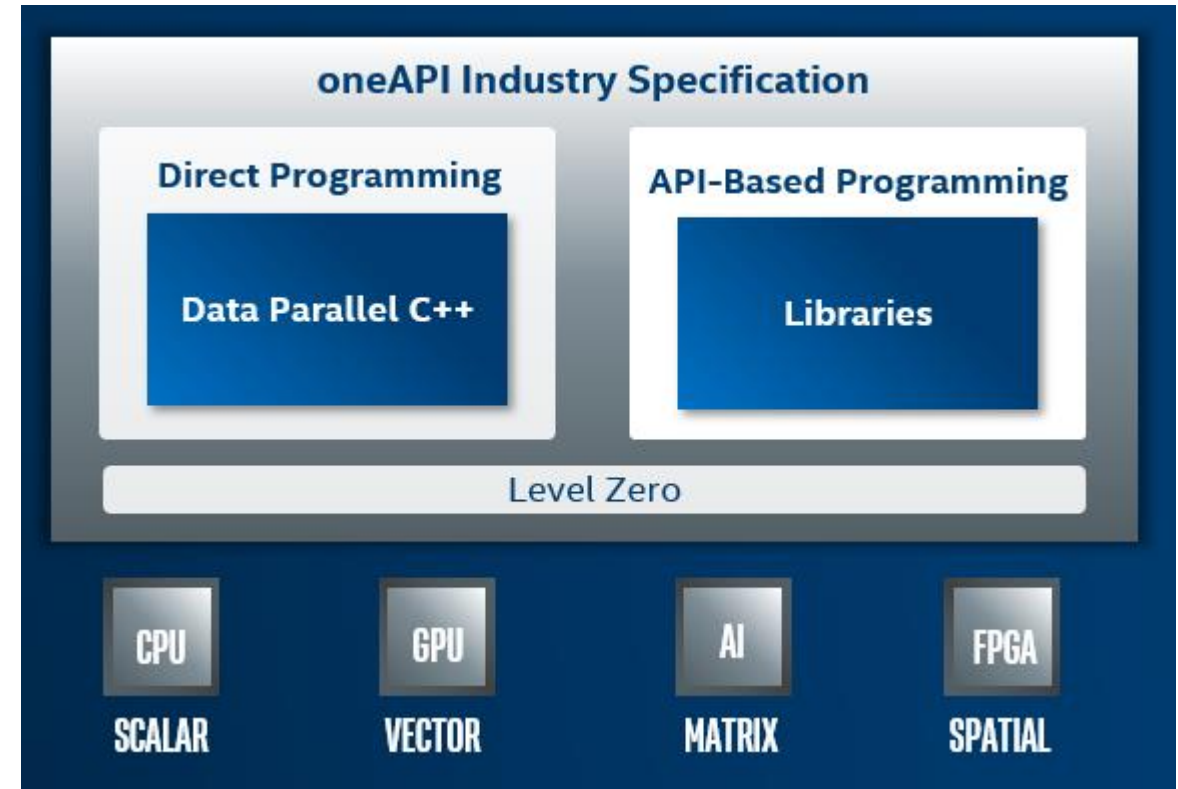
Unified and simplified language and libraries for
expressing parallelism

Uncompromised native high-level language
performance

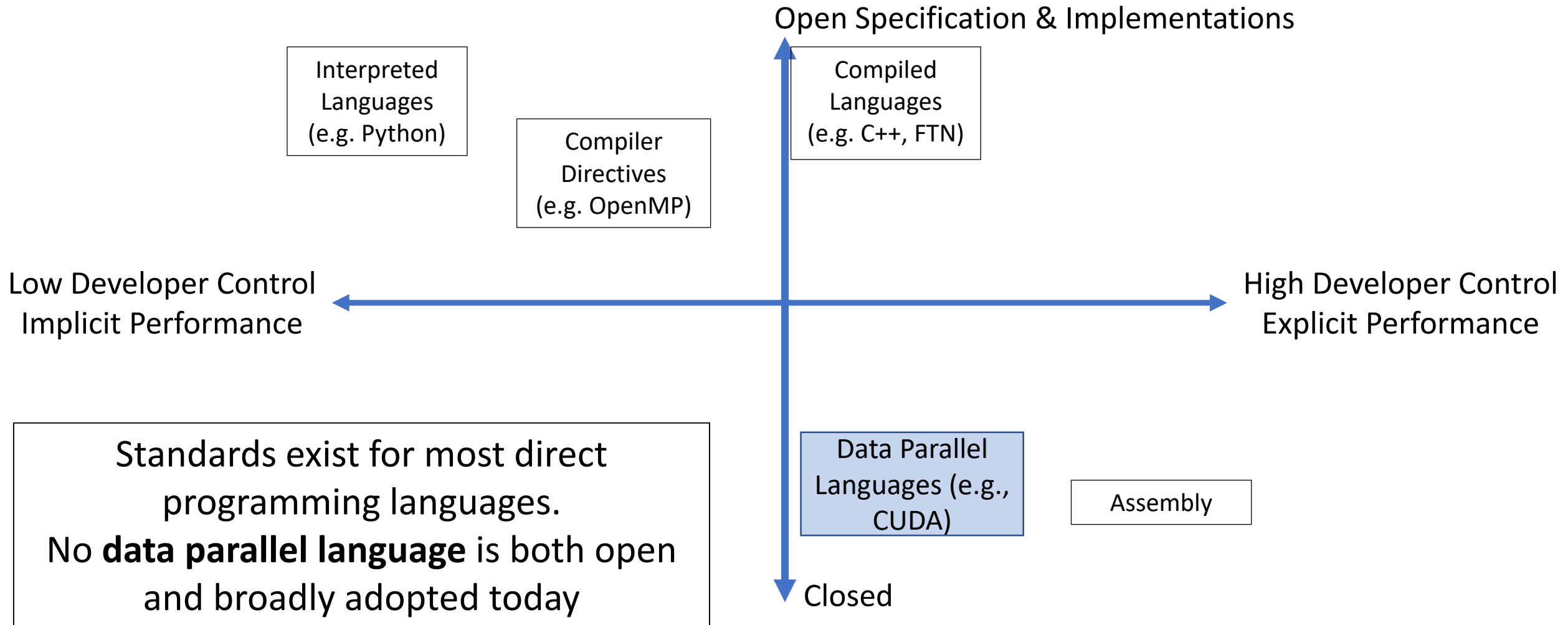
Interoperates with existing languages and libraries

Support for CPU, GPU, AI and FPGA

Based on industry standards and open specifications

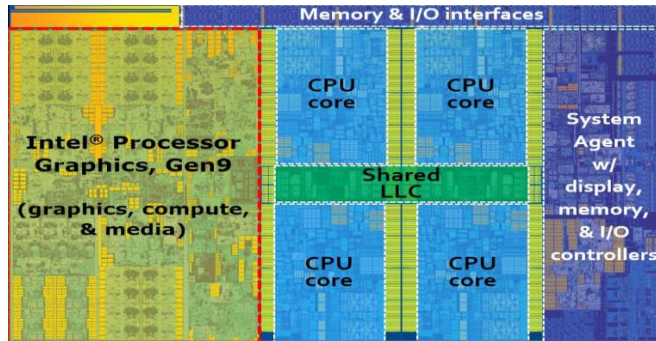


The direct programming problem

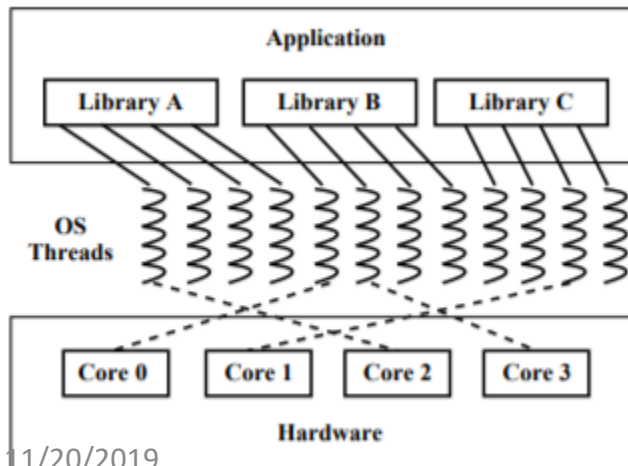


The API programming problem

Where to run?

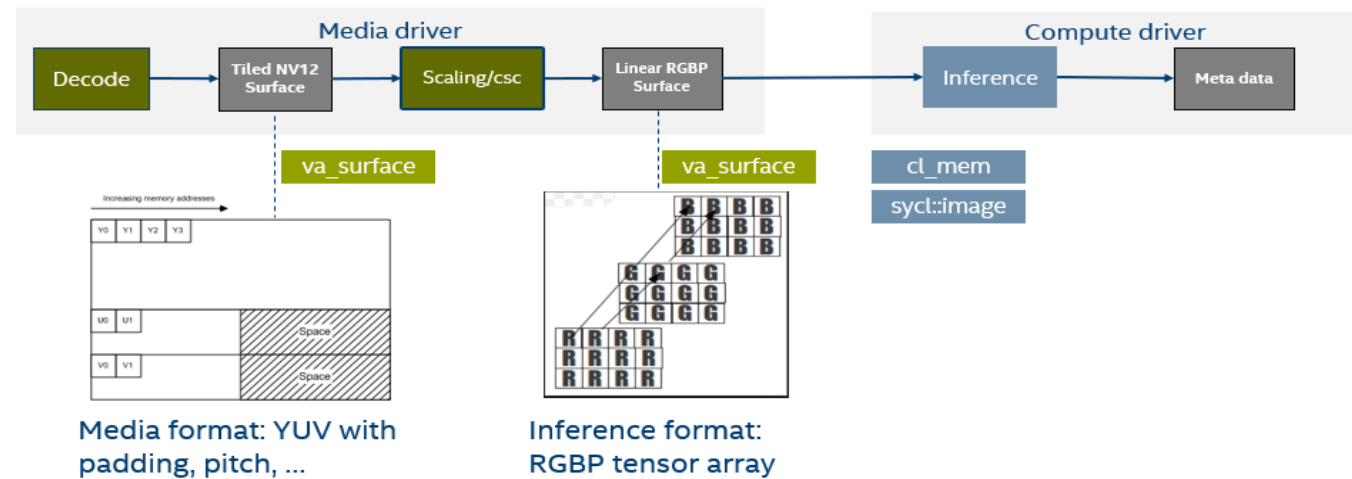


Sharing resources?



Sharing data?

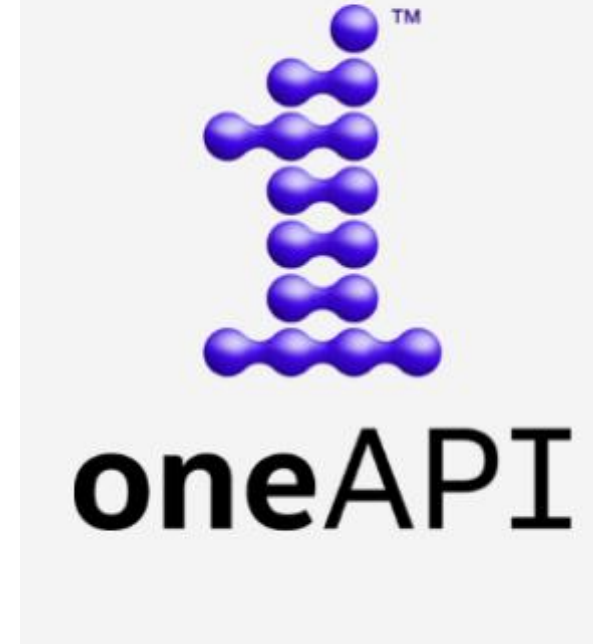
Media produces NV12 while Compute/Inference takes RGBP image format



oneAPI industry initiative

Open Industry Specification

- Specifies Language, APIs, Low level Hardware Interface
- Cooperative relationship with Khronos SYCL standard
- Promotes community and industry support
- Supports code reuse across architectures and vendors



<https://www.oneapi.com/>

ONEAPI INITIATIVE – ECOSYSTEM SUPPORT

allegro.ai

CINECA



GIGASPACE



Taboola



Hewlett Packard
Enterprise

Lenovo

Tencent 腾讯

RENIA

Argonne
NATIONAL LABORATORY

CERN
openlab

codeplay®

MEGWARE



sas



Laboratório
Nacional de
Computação
Científica



Indian Institute of
Technology Delhi



MEGH
COMPUTING

Tech
Mahindra

UNIVERSITY OF
CAMBRIDGE

ANSYS®



Stockholm
University



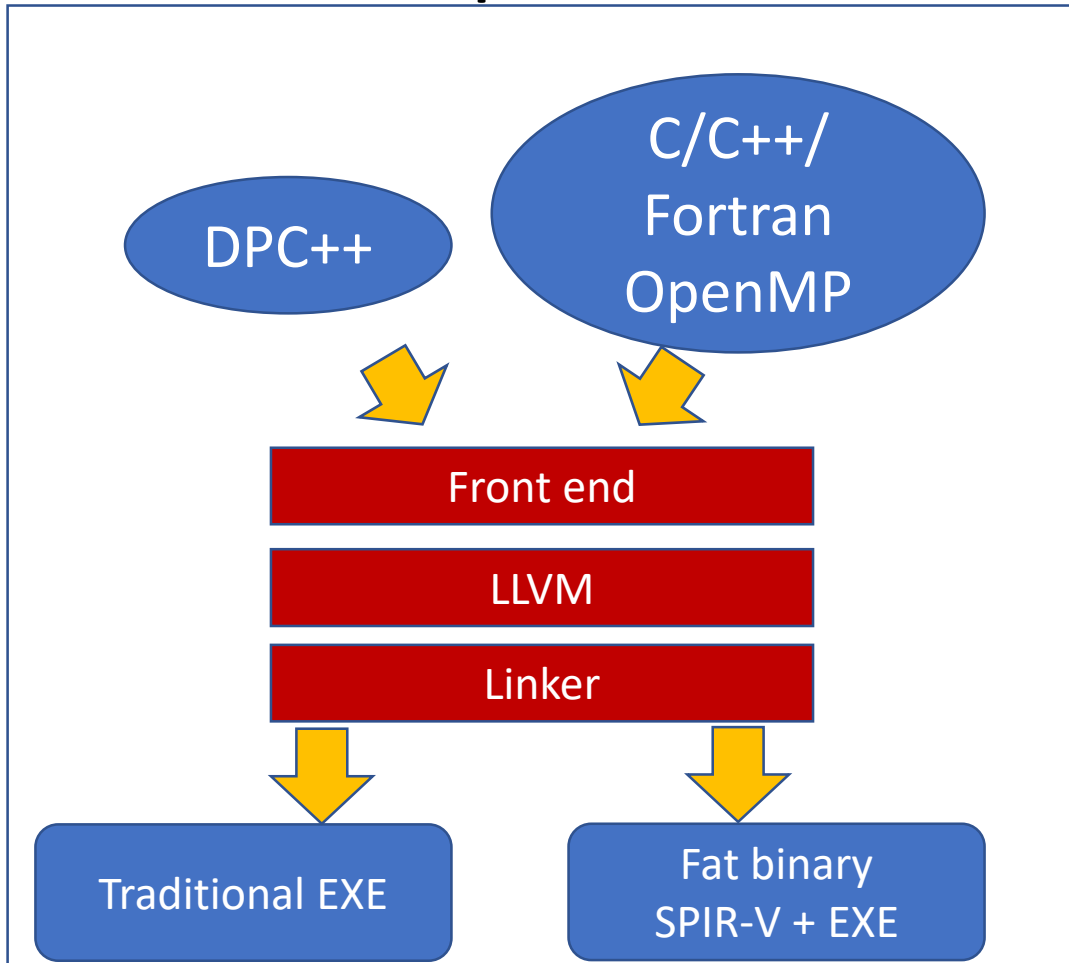
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These organizations support the oneAPI initiative 'concept' for a single, unified programming model for cross-architecture development. It does not indicate any agreement to purchase or use of Intel's products.

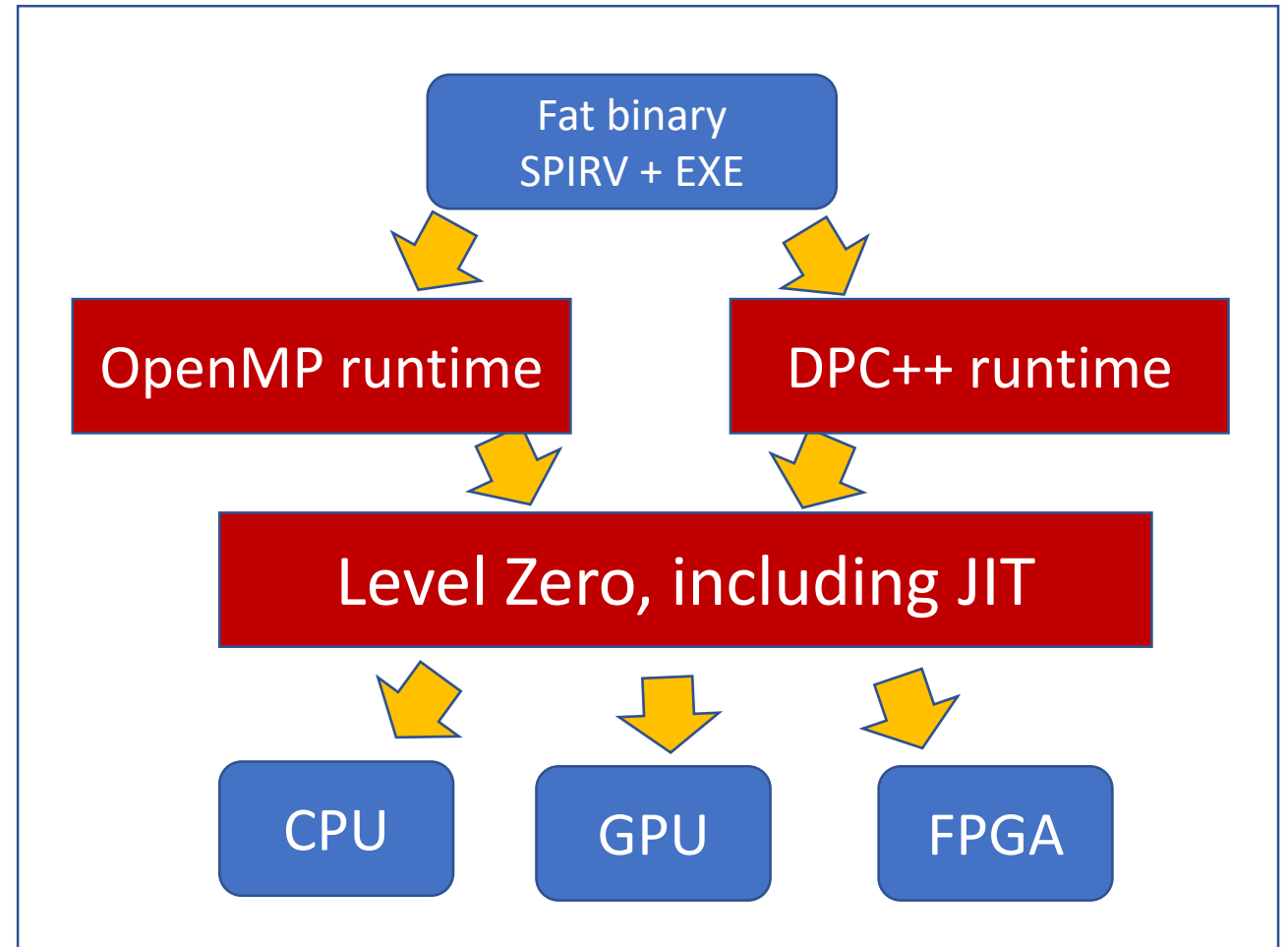
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Common stack for DPC++ and OpenMP

Compile-time



Run-time



Optimized MPI Operation with device RDMA

```
MPI_Send(
```

```
  dA,
```

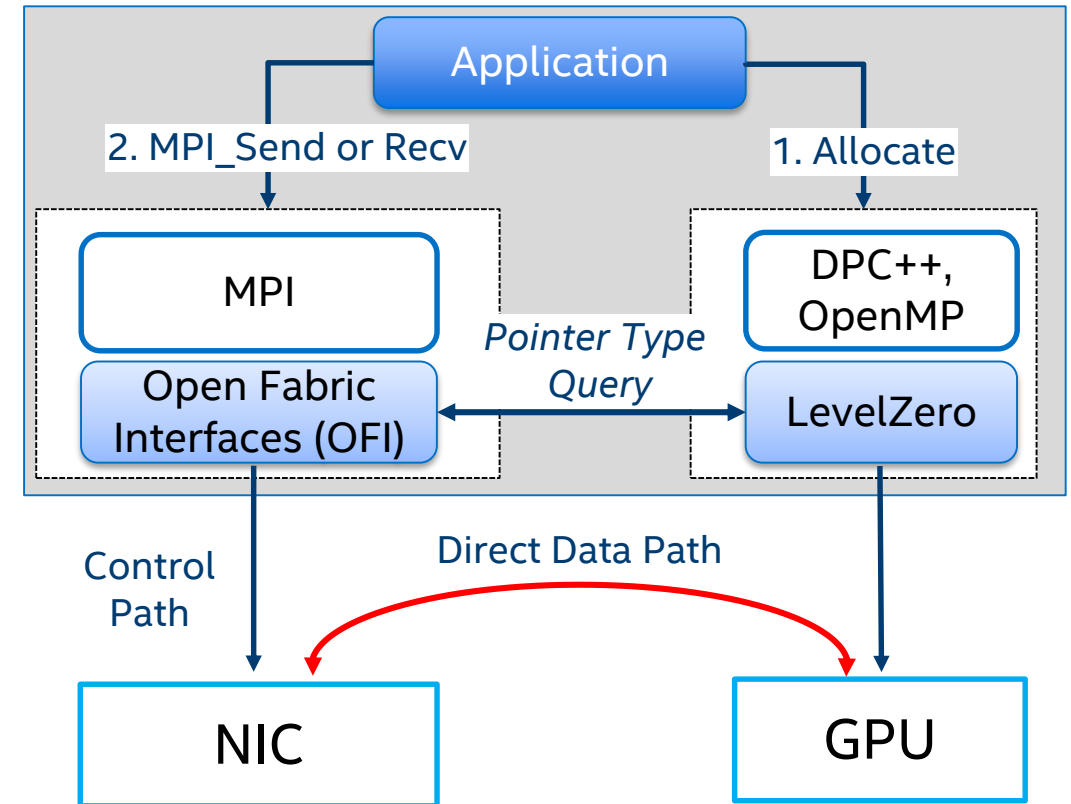
If address is in device memory,
direct path to NIC is used

```
  N,
```

```
  MPI_FLOAT,
```

```
  remote_rank, tag,  
  communicator);
```

```
}
```



ONEAPI AVAILABLE NOW ON INTEL DEVCLOUD

A development sandbox to develop, test and run your workloads across a range of Intel CPUs, GPUs, and FPGAs using Intel's oneAPI beta software

software.intel.com/en-us/devcloud/oneapi

Learn about oneAPI Toolkits

Learn Data Parallel C++

Evaluate Workloads

Build Heterogenous Applications

Prototype your project

NO DOWNLOADS | NO HARDWARE ACQUISITION | NO INSTALLATION | NO SET-UP AND CONFIGURATION
GET UP AND RUNNING FAST!

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